

# SPECIFICATIONS FOR LCD MODULE

Module No. JHB12232D

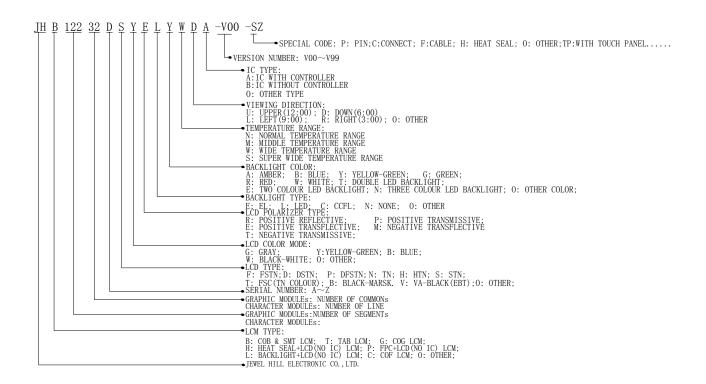
E-mail: sales@jhlcd.com Website: www.jhlcd.com

JHB12232D VER: 3.01 - 0 - Issue date: 2013/08/01

# TABLE OF CONTENTS

| LC  | M NUMBER SYSTEM                   | 2  |
|-----|-----------------------------------|----|
| 1.  | GENERAL DESCRIPTION               | 3  |
| 2.  | FEATURES                          | 3  |
| 3.  | MECHANICAL SPECIFICATION          | 3  |
| 4.  | MECHANICAL DIMENSION              | 4  |
| 5.  | MAXIMUM RATINGS                   | 5  |
| 6.  | ELECTRICAL CHARACTERISTICS        | 5  |
| 7.  | MODULE FUNCTION DESCRIPTION       | 6  |
| 8.  | ELECTRO-OPTICAL CHARACTERISTICS   | 12 |
| 9.  | RELIABILITY                       | 16 |
| 10. | PRECAUTIONS FOR USING LCD MODULES | 17 |
| 11. | USING LCD MODULES                 | 19 |
| 12. | REVISION HISTORY                  | 21 |
| SAI | MPLE APPROVED REPORT              | 22 |

# **LCM Number System**



JHB12232D VER: 3.01 - 2 - Issue date: 2013/08/01

### 1. GENERAL DESCRIPTION

The JHB12232D is a 122 x 32 Dots Graphics LCD module. It has a STN panel composed of 122 segments and 32 commons. The LCM can be easily accessed by micro-controller via parallel interface.

### 2. FEATURES

| Diaglass Mada      | Transflective and positive         |  |  |  |  |  |  |
|--------------------|------------------------------------|--|--|--|--|--|--|
| Display Mode       | STN(Y-G) module                    |  |  |  |  |  |  |
| Display Format     | Graphic 122 x 32 dots              |  |  |  |  |  |  |
| Input Data         | 8-bit parallel data input from MPU |  |  |  |  |  |  |
| Multiplexing Ratio | /32 Duty                           |  |  |  |  |  |  |
| Bias               | 1/6 Bias                           |  |  |  |  |  |  |
| Viewing Direction  | 6 O'clock                          |  |  |  |  |  |  |
| Backlight          | LED(Y-G)                           |  |  |  |  |  |  |
| Driver IC          | SBN1661 or substitute              |  |  |  |  |  |  |

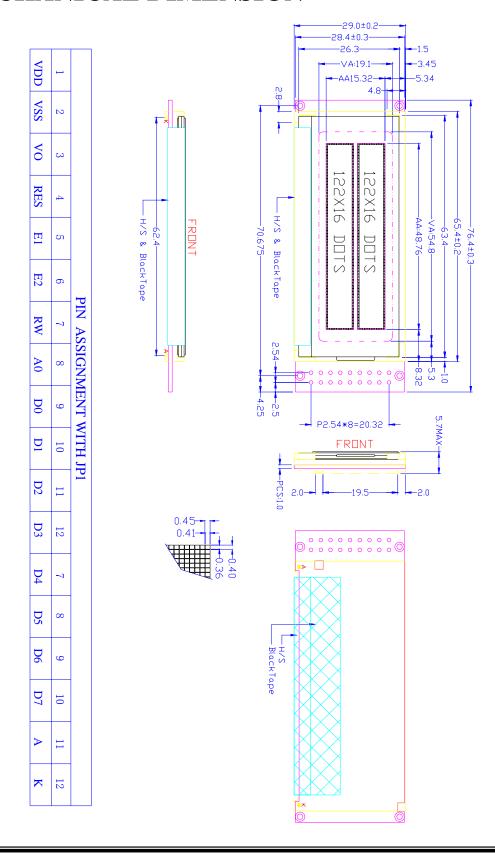
# 3. MECHANICAL SPECIFICATION

| Item                | Specifications       | Unit |
|---------------------|----------------------|------|
| Dimensional outline | 76.4x29.0 x 5.7(max) | mm   |
| Resolution          | 122segs x 32coms     | dots |
| Viewing area        | 54.8(W) x 19.1(H)    | mm   |
| Active area         | 48.76(W) x 15.32(H)  | mm   |
| Dots pitch          | 0.4 (W)×0.45(H)      | mm   |
| Dots size           | 0.36(W)×0.41(H)      | mm   |

JHB12232D VER: 3.01 - 3 - Issue date: 2013/08/01



# 4. MECHANICAL DIMENSION



JHB12232D VER: 3.01 - 4 - Issue date: 2013/08/01

### 5. MAXIMUM RATINGS

| Item                  | Symbol                            | Min  | Max                  | Unit                   | Note |
|-----------------------|-----------------------------------|------|----------------------|------------------------|------|
| g 1                   | V <sub>DD</sub> - V <sub>SS</sub> | -0.3 | 7.0                  | V                      |      |
| Supply voltage        | $V_{\scriptscriptstyle LCD}$      | -    | 13.0                 | V                      |      |
| Input Voltage         | $V_{\rm IN}$                      | -0.3 | V <sub>DD</sub> +0.3 | V                      |      |
| Operating temperature | $T_{OPR}$                         | -20  | +70                  | $^{\circ}\!\mathbb{C}$ |      |
| Storage temperature   | $T_{STR}$                         | -30  | +80                  | $^{\circ}\!\mathbb{C}$ |      |
| Humidity              |                                   |      | 90                   | %RH                    |      |

# 6. ELECTRICAL CHARACTERISTICS

| Item                            |         | Symbol                               | Condition   | Min.              | Тур. | Max.                            | Unit |
|---------------------------------|---------|--------------------------------------|---|-------------------|------|---------------------------------|------|
| Supply Voltage                  | Logic   | $V_{\scriptscriptstyle DD}$          |   |                   | 5.0  | 5.5                             | V    |
| Innut Voltage                   | H level | $V_{\mathrm{IH}}$                    |   | $V_{\rm DD}$ -2.2 |      | $V_{\scriptscriptstyle DD}$     | V    |
| Input Voltage                   | L level | $V_{\scriptscriptstyle \mathrm{IL}}$ |   | $V_{ss}$          |      | $0.8_{\scriptscriptstyle  m D}$ | V    |
| Current Consur<br>(LCD DRIV     | -       | $ m I_{DD}$                          | $V_{\text{DD}}$ =5.0V; $V_{\text{LCD}}$ =4.5V, $T_{\text{amb}}$ =25°C;                      |                   |      | 0.5                             | mA   |
| LCD Driving V                   | oltage  | $V_{\scriptscriptstyle LCD}$         | Bias=1/5<br>VLCD=VDD-VO   | 4.3               | 4.5  | 4.7                             | V    |
| Power Supply fo                 | or LED  | V <sub>f</sub>                       | If=100mA  | 4.8               | 5.0  | 5.2                             | V    |
| Current Consul<br>(With LED Bac | •       | If                                   | $V_{\text{DD}} = 5.0 \text{V};$ $V_f = 5.0 \text{V}, T_{\text{amb}} = 25 ^{\circ}\text{C};$ |                   | 100  | 120                             | mA   |

JHB12232D VER: 3.01 - 5 - Issue date: 2013/08/01



# 7. MODULE FUNCTION DESCRIPTION

### 7.1. PIN DESCRIPTION

| Pin No. | Symbol | Description  |
|---------|--------|--|
| 1       | VDD    | Power supply for positive (5.0V)                           |
| 2       | VSS    | Power supply for Ground (0V)                               |
| 3       | VLCD   | LCD driver voltage regulation terminal                     |
| 4       | RES    | Reset signal input pin, Low level is active                |
| 5       | E1     | Dood/write anchle signal input nin                         |
| 6       | E2     | Read/write enable signal input pin                         |
| 7       | RW     | Read/write selection signal, R/W="H": Read; R/W="L": Write |
| 8       | A0     | Data/command signed select, RS="H": Data; RS="L": Command  |
| 9       | D0     |  |
| 10      | D1     |  |
| 11      | D2     |  |
| 12      | D3     | -8-bit bi-directional data bus                             |
| 13      | D4     | 8-bit bi-directional data bus                              |
| 14      | D5     |  |
| 15      | D6     |  |
| 16      | D7     |  |
| 17      | LEDA   | Backlight power supply voltage for positive                |
| 18      | LEDK   | Backlight power supply voltage for negative                |

JHB12232D VER: 3.01 - 6 - Issue date: 2013/08/01

### 7.2 TIMING CHARACTERISTICS

#### 1. SYSTEM BUS READ/WRITE CHARACTERISTIC

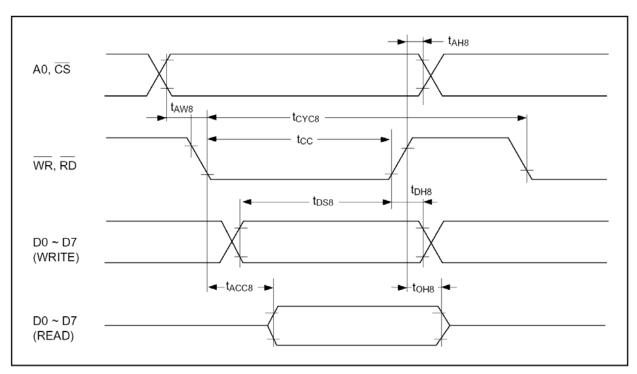
#### System Bus Read/Write I (80-family MPU)

 $T_a = -20 \text{ to } 75^{\circ}\text{C}, \text{ Vss} = -5.0\text{V} \pm 10\%$ 

| Parameter           | Symbol | Signal  | Condition   | Min  | Тур | Max | Unit |
|---------------------|--------|---------|-------------|------|-----|-----|------|
| Address hold time   | tah8   | A0, CS  |             | 10   |     | _   | ns   |
| Address setup time  | tAW8   | A0, C5  |             | 20   |     | _   | ns   |
| System cycle time   | tcyc8  | WR, RD  |             | 1000 | _   | _   | ns   |
| Control pulse width | tcc    | WK, KD  |             | 200  |     | _   | ns   |
| Data setup time     | tDS8   |         |             | 80   | _   | _   | ns   |
| Data hold time      | tDH8   | D0 – D7 |             | 10   | _   | _   | ns   |
| RD access time      | tACC8  | 00-07   | CL = 100 pF | _    |     | 90  | ns   |
| Output disable time | toн8   |         | CL = 100 pF | 10   | _   | 60  | ns   |

<sup>\*1</sup> Each of the values where Vss = -3.0V is about 200% of that where Vss = -5.0V (i.e., the listed value).

<sup>\*2</sup> The rise or fall time of input signals should be less than 15 ns.



System bus read/write I (80-family MPU)

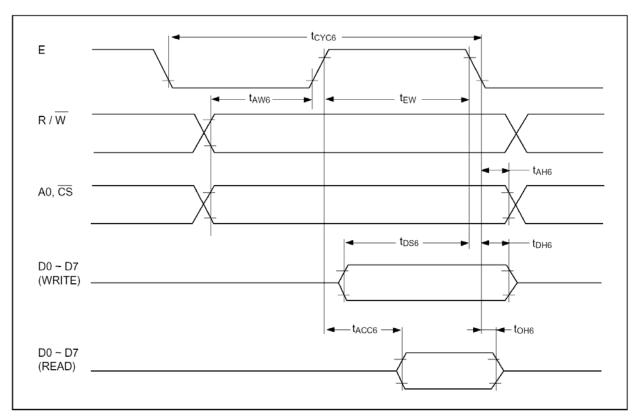
JHB12232D VER: 3.01 - 7 - Issue date: 2013/08/01

#### System Bus Read/Write II (68-family MPU)

 $T_a = -210 \text{ to } 75^{\circ}\text{C}, \text{ Vss} = -5.0\text{V} \pm 10\%$ 

| Parameter                 | Symbol   | Signal  | Condition   | Min  | Тур | Max | Unit |
|---------------------------|----------|---------|-------------|------|-----|-----|------|
| System cycle time         | tCYC6 *1 | A0, CS  |             | 1000 | _   | _   | ns   |
| Address setup time        | tAW6     | R/W     |             | 20   | _   | _   | ns   |
| Address hold time         | tAH6     | K/W     |             | 10   | _   | _   | ns   |
| Data setup time           | tDS6     |         |             | 80   | _   | _   | ns   |
| Data hold time            | tDH6     | D0 D7   |             | 10   | _   | _   | ns   |
| Output disable time       | tOH6     | D0 – D7 | CL = 100 pF | 10   | _   | 60  | ns   |
| Access time               | tACC6    |         | CL = 100 pF | _    | _   | 90  | ns   |
| Enable pulse width: Read  | 45144    | _       |             | 100  | _   | _   | ns   |
| Enable pulse width: Write | tEW      | E       |             | 80   | _   | _   | ns   |

- \*1 toyos indicates the cycle time during which  $\overline{CS} \cdot E = "H"$ . It does not mean the cycle time of signal E.
- \*2 Each of the values where Vss = -3.0V is about 200% of that where Vss = -5.0V (i.e., the listed value).
- \*3 The rise or fall time of input signals should be less than 15 ns.



System bus read/write II (68-family MPU)

JHB12232D VER: 3.01 - 8 - Issue date: 2013/08/01

#### 2. DISPLAY CONTROL OUTPUT TIMING

#### **Input Timing**

 $T_a = -20 \text{ to } 75^{\circ}\text{C}, \text{ Vss} = -5.0\text{V} \pm 10\%$ 

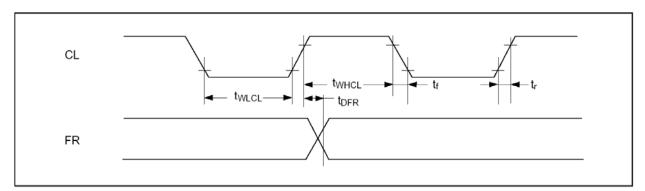
| Parameter              | Symbol | Signal | Condition | Min  | Тур | Max | Unit |
|------------------------|--------|--------|-----------|------|-----|-----|------|
| Low level pulse width  | twlcl  |        |           | 35   |     | _   | μs   |
| High level pulse width | twhcl  | CL     |           | 35   | _   | _   | μs   |
| Rise time              | tr     |        |           | _    | 30  | 150 | ns   |
| Fall time              | tf     |        |           | _    | 30  | 150 | ns   |
| FR delay time          | tDFR   | FR     |           | -2.0 | 0.2 | 2.0 | μs   |

#### **Output Timing**

 $T_a = -20 \text{ to } 75^{\circ}\text{C}, \text{ Vss} = -5.0\text{V} \pm 10\%$ 

| Parameter     | Symbol | Signal | Condition   | Min | Тур | Max | Unit |
|---------------|--------|--------|-------------|-----|-----|-----|------|
| FR delay time | tDFR   | FR     | CL = 100 pF | _   | 0.2 | 0.4 | μs   |

- \*1. The listed FR input delay time applies to the SED1521 and 1520 (slave). The listed FR output delay time applies to the SED1520 (master).
- \*2. Each of the values where Vss = -3.0V is about 200% of that where Vss = -5.0V (i.e., the listed value).

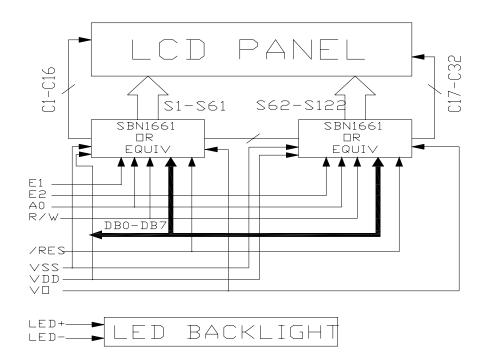


Display control timing

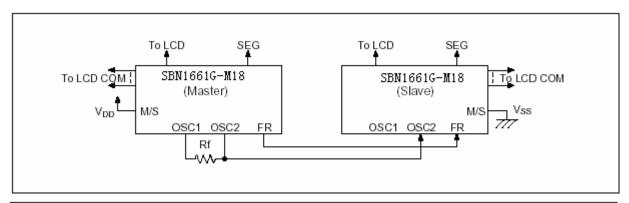
JHB12232D VER: 3.01 - 9 - Issue date: 2013/08/01

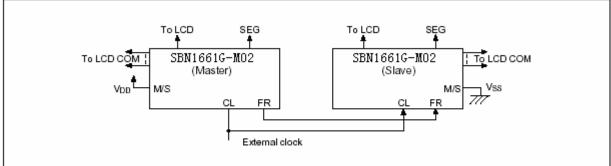
### 7.3 APPLICATION OF LCM

### ■Circuit Block Diagram



### ■Reference circuit





JHB12232D VER: 3.01 - 10 - Issue date: 2013/08/01

#### 7.4 TABLE OF COMMAND

Table lists the commands used with the SBN1661 This LSI uses a combination of A0, R/W, (RD, WR) to identify a data bus signal. Interpretation and execution of a command depends not on external clock but on internal timing alone. Therefore, a command can be executed so fast that no busy check is needed.

#### Table Commands

|      | Command                         | Code |    |    |      |     |       |       |              | E              | nction   |       |  |  |  |  |
|------|---------------------------------|------|----|----|------|-----|-------|-------|--------------|----------------|----------|-------|--|--|--|--|
|      | Command                         | Α0   | RD | WR | D7   | D6  | D5    | D4    | D3           | D2             | D1       | D0    | rui  | iction                                       |  |  |
| (1)  | Display ON/OFF                  | 0    | 1  | 0  | 1    | 0   | 1     | 0     | 1            | 1              | 1        | 0/1   | Turns all display on or<br>display RAM data or in  |  |  |  |
|      |                                 |      |    |    |      |     |       |       |              |                |          |       | ON     OFF (Power-saving mode with static drive on)*   |  |  |  |
| (2)  | Display Start Line              | 0    | 1  | 0  | 1    | 1   | 0     | А     | Disp<br>ddre | lay S<br>ss (C |          |       | Specifies RAM line corresponding to uppermost<br>line (COM0) of display.   |  |  |  |
| (3)  | Set Page Address                | 0    | 1  | 0  | 1    | 0   | 1     | 1     | 1            | 0              | Page     | (0-3) | Sets display RAM pag   | e in page address register.                  |  |  |
| (4)  | Set Column (Segment)<br>Address | 0    | 1  | 0  | 0    | (   | Colu  | mn A  | ddre         | es (C          | )–79     | )     | Sets display RAM colu<br>address register.   | mn address in column                         |  |  |
| (5)  | Read Status                     | 0    | 0  | 1  | Sy   | Ω   | щ     | H     | 0            | 0              | 0        | 0     | Reads the following sta  | atus:  |  |  |
|      |                                 |      |    |    | Busy | ADC | ONOFF | RESET |              |                |          |       | BUSY 1: Internal operation 0: Ready ADC 1: CW output (forward) 0: CCW output (reverse) ON/OFF1: Display off RESET 1: Being reset 0: Normal |  |  |  |
| (6)  | Write Display Data              | 1    | 1  | 0  |      |     | ١     | Vrite | Data         | a              | <u> </u> |       | Writes data from data Display RAM location bus into display RAM, whose address has been  |  |  |  |
| (7)  | Read Display Data               | 1    | 0  | 1  |      |     | F     | Read  | i Dat        | а              |          |       | Reads data from display RAM onto data bus.  Preset is accessed. After access, the column address is incremented by 1.                      |  |  |  |
| (8)  | Select ADC                      | 0    | 1  | 0  | 1    | 0   | 1     | 0     | 0            | 0              | 0        | 0/1   | Used to invert relations<br>between display RAM<br>segment driver outputs  | column addresses and                         |  |  |
|      |                                 |      |    |    |      |     |       |       |              |                |          |       | 0: CW output (forward)<br>1: CCW output (revers  |  |  |  |
| (9)  | Static Drive ON/OFF             | 0    | 1  | 0  | 1    | 0   | 1     | 0     | 0            | 1              | 0        | 0/1   | Selects normal display<br>1: Static drive (power-s<br>0: Normal driving  | or static driving operation.<br>saving mode) |  |  |
| (10) | Select Duty                     | 0    | 1  | 0  | 1    | 0   | 1     | 0     | 1            | 0              | 0        | 0/1   | Selects LCD cell drivin  | g duty                                       |  |  |
|      | -                               |      |    |    |      |     |       |       |              |                |          |       | 1: 1/32<br>0: 1/16   |  |  |  |
| (11) | Read Modify Write               | 0    | 1  | 0  | 1    | 1   | 1     | 0     | 0            | 0              | 0        | 0     | Increments column address counter by 1 when display data is written. (This is not done when data is read.)                                 |  |  |  |
| (12) | End                             | 0    | 1  | 0  | 1    | 1   | 1     | 0     | 1            | 1              | 1        | 0     | Clears read modify wri   | te mode.                                     |  |  |
| (13) | Reset                           | 0    | 1  | 0  | 1    | 1   | 1     | 0     | 0            | 0              | 1        | 0     | Sets display start line register on the first line. Also sets column address counter and page address counter to 0.                        |  |  |  |

<sup>\*</sup> With display off (command (1)), static drive going on (9) invokes power-saving mode.

A detailed description of all the commands follows.

JHB12232D VER: 3.01 - 11 - Issue date: 2013/08/01

### 8. ELECTRO-OPTICAL CHARACTERISTICS

| Item                | Symbol          | Condition           | Temp                | Min                 | Тур.                | Max                 | Units               | Note                |                     |                     |                     |                     |                     |                     |                     |                     |      |  |     |     |      |
|---------------------|-----------------|---------------------|---------------------|---------------------|---------------------|---------------------|---------------------|---------------------|---------------------|---------------------|---------------------|---------------------|---------------------|---------------------|---------------------|---------------------|------|--|-----|-----|------|
| 1 CD 1              |                 |                     | 0°C                 | -                   | 4.7                 | 1                   |                     |                     |                     |                     |                     |                     |                     |                     |                     |                     |      |  |     |     |      |
| LCD driving voltage | VLCD            | $\theta = \phi = 0$ | 25℃                 | 4.2                 | 4.5                 | 4.7                 | V                   | NOTE1               |                     |                     |                     |                     |                     |                     |                     |                     |      |  |     |     |      |
|                     |                 |                     | 50°C                | -                   | 4.3                 | -                   |                     |                     |                     |                     |                     |                     |                     |                     |                     |                     |      |  |     |     |      |
|                     | Rise Time (Tr)  |                     | 0°C                 |                     |                     |                     |                     |                     |                     |                     |                     |                     |                     |                     |                     |                     |      |  |     |     |      |
|                     | Decay Time (Tf) | $\theta = \phi = 0$ |                     |                     |                     |                     |                     |                     |                     |                     |                     |                     |                     |                     |                     |                     |      |  |     |     |      |
|                     | Rise Time (Tr)  |                     | 25°C                |                     | 225                 | 340                 |                     |                     |                     |                     |                     |                     |                     |                     |                     |                     |      |  |     |     |      |
| Response Time       | Decay Time (Tf) |                     | $\theta = \phi = 0$ | 25°C |  | 240 | 360 | msec |
|                     | Rise Time (Tr)  |                     | <b>70°</b> C        |                     |                     |                     |                     |                     |                     |                     |                     |                     |                     |                     |                     |                     |      |  |     |     |      |
|                     | Decay Time (Tf) |                     | 50°C                |                     |                     |                     |                     |                     |                     |                     |                     |                     |                     |                     |                     |                     |      |  |     |     |      |
| Contrast Ratio      | Cr              | $\theta = \phi = 0$ | 25°C                | 5                   | 10                  |                     |                     | NOTE4               |                     |                     |                     |                     |                     |                     |                     |                     |      |  |     |     |      |

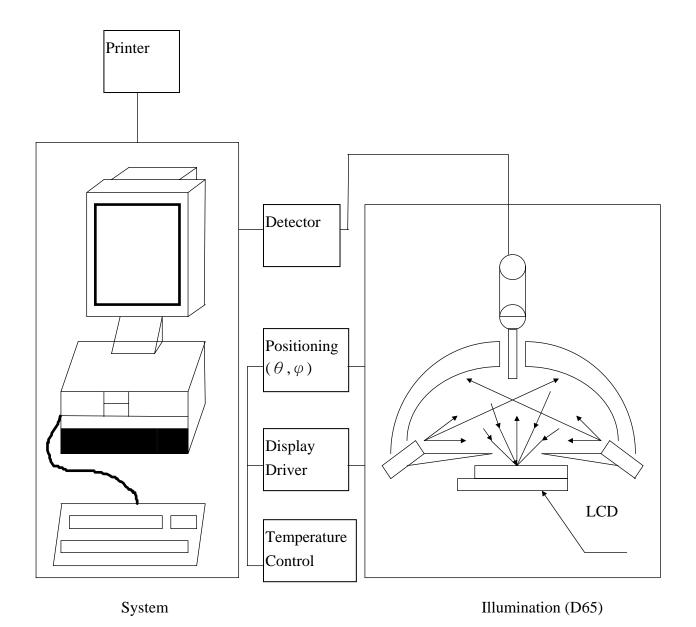
| Viewing Angle<br>Range | $\theta (\phi = 0^{\circ})$ (6") | $\phi = 90^{\circ}$ (3") | φ=180° (12") | φ=270°<br>(9") | 備註           |
|------------------------|----------------------------------|--------------------------|--------------|----------------|--------------|
| θ (25°C)<br>CR≥2       | 45                               | 35                       | 30           | 20             | Deg<br>NOTE3 |

• For panel only

JHB12232D VER: 3.01 - 12 - Issue date: 2013/08/01



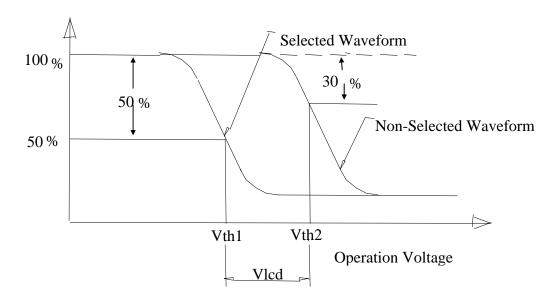
### • Electro-Optical Characteristics Measuring Equipment(DMS501)



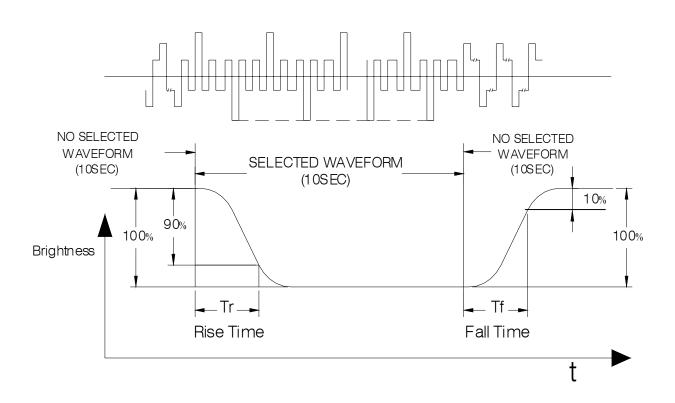
JHB12232D VER: 3.01 - 13 - Issue date: 2013/08/01



### • Note 1. Definition of Driving Voltage(Vlcd):



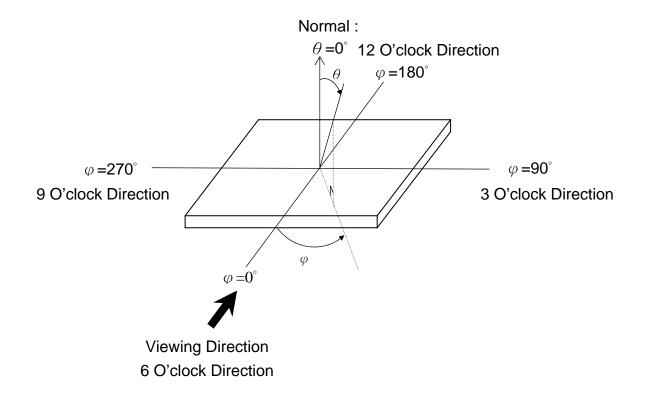
### • Note 2. Definition of Optical Response Time :



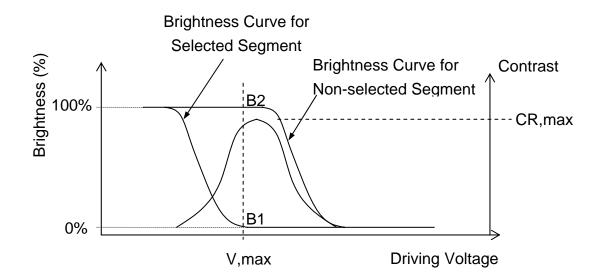
JHB12232D VER: 3.01 - 14 - Issue date: 2013/08/01



### • Note 3. Definition of Viewing Angle $\,\theta\,$ and $\,\phi\,$ :



### • Note 4. Definition of Contrast ratio (CR):



JHB12232D VER: 3.01 - 15 - Issue date: 2013/08/01

# 9. RELIABILITY

### 9.1. MTBF

The LCD module shall be designed to meet a minimum MTBF value of 30000 hours with normal. (25°C in the room without sunlight)

### **9.2. TESTS**

| NO. | ITEM  | CONDITION                                  | CRITERION   |
|-----|---|--|---|
| 1   | High Temperature Operating                  | 70°C 120Hrs                                | <ul><li>No Defect Of</li><li>Operational Function In</li><li>Room Temperature Are</li></ul> |
| 2   | Low Temperature Operating                   | -20°C 120Hrs                               | Allowable.  • IDD of LCM in   |
| 3   | High Temperature/<br>Humidity Non-Operating | 60°C ,90%RH ,120 Hrs                       | Pre-and post-test should follow specification   |
| 4   | High Temperature<br>Non-Operating           | 80°C 120Hrs                                |   |
| 5   | Low Temperature<br>Non-Operating            | -30°C 120Hrs                               |   |
| 6   | Temperature Cycling Non-Operating           | -20°C (30Min ) ↔ 60°C (30Min)<br>10 CYCLES |   |

Notes: Judgments should be mode after exposure in room temperature for two hours.

### 10. PRECAUTIONS FOR USING LCD MODULES

### 10.1. HANDLING PRECAUTIONS

- (1) The display panel is made of glass. Do not subject it to a mechanical shock or impact by dropping it.
- (2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- (3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- (4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- (5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten a cloth with one of the following solvents:
  - Isopropyl alcohol
  - Ethyl alcohol
- (6) Solvents other than those above mentioned may damage the polarizer.

Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- (7) Extra care to minimize corrosion of the electrode. Water droplets, moisture condensation or a current flow in a high-humidity environment accelerates corrosion of the electrode.
- (8) Install the LCD Module by using the mounting holes. When mounting the LCD Module, make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- (9) Do not attempt to disassemble or process the LCD Module.
- (10) NC terminal should be open. Do not connect anything.
- (11) If the logic circuit power is off, do not apply the input signals.
- (12) To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
  - Be sure to ground the body when handling he LCD Module.
  - Tools required for assembling, such as soldering irons, must be properly grounded.
  - -To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions.

JHB12232D VER: 3.01 - 17 - Issue date: 2013/08/01

-The LCD Module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

#### 10.2. STORAGE CONDITIONS

When storing, avoid the LCD module to be exposed to direct sunlight of fluorescent lamps. For stability, to keep it away form high temperature and high humidity environment (The best condition is : 23±5°C, 45±20%RH). ESD protection is necessary for long-term storage also.

### 10.3. OTHERS

Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.

If the LCD Module have been operating for a long time showing the same display patterns the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be recovered by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.

To minimize the performance degradation of the LCD Module resulting from destruction caused by static electricity etc. exercise care to avoid holding the following sections when handling the modules.

- Exposed area of the printed circuit board.
- Terminal electrode sections.

JHB12232D VER: 3.01 - 18 - Issue date: 2013/08/01

# 11. Using LCD modules

### 11.1 LIQUID CRYSTAL DISPLAY MODULES

LCD is composed of glass and polarizer. Pay attention to the following items when handling.

- (1) Please keep the temperature within specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.
- (2) Do not touch, push or rub the exposed polarizers with anything harder than a HB pencil lead (glass, tweezers, etc).
- (3) N-hexane is recommended for cleaning the adhesives used to attach front/rear polarizers and reflectors made of organic substances, which will be damaged by chemicals such as acetone, toluene, toluene, ethanol and isopropyl alcohol.
- (4) When the display surface becomes dusty, wipe gently with absorbent cotton or other soft material like chamois soaked in petroleum ether. Do not scrub hard to avoid damaging the display surface.
- (5) Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading.
- (6) Avoid contacting oil and fats.
- (7) Condensation on the surface and contact with terminals due to cold will damage, stain or polarizers. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.
- (8) Do not put or attach anything on the display area to avoid leaving marks on.
- (9) Do not touch the display with bare hands. This will stain the display area and degrade insulation between terminals (some cosmetics are determinate to the polarizers).
- (10)As glass is fragile, it tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring.

#### 11.2 INSTALLING LCD MODULE

Attend to the following items when installing the LCM.

- (1) Cover the surface with a transparent protective plate to protect the polarizer and LC cell.
- (2) When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be  $\pm 0.1$ mm.

#### 11.3 ELECTRO-STATIC DISCHARGE CONTROL

Since this module uses a CMOS LSI, the same careful attention should be paid for electrostatic discharge as for an ordinary CMOS IC.

JHB12232D VER: 3.01 - 19 - Issue date: 2013/08/01



- (1) Make certain that you are grounded when handing LCM.
- (2) Before removing LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential.
- (3) When soldering the terminal of LCM, make certain the AC power source for the soldering iron does not leak.
- (4) When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
- (5) As far as possible, make the electric potential of your work clothes and that of the workbenches to the ground potential.
- (6) To reduce the generation of electro-static discharge, be careful that the air in the work is not too dried. A relative humidity of 50%-60% is recommended.

#### 11.4 PRECAUTIONS FOR OPERATION

- (1) Viewing angle varies with the change of liquid crystal driving voltage (Vo). Adjust Vo to show the best contrast.
- (2) Driving the LCD in the voltage above the limit will shorten its lifetime.
- (3) Response time is greatly delayed at temperature below the operating temperature range. However, this does not mean the LCD will be out of the order. It will recover when it returns to the specified temperature range.
- (4) If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then on.
- (5) Condensation on terminals can cause an electrochemical reaction disrupting the terminal circuit. Therefore, this product must be used and stored within the specified condition of 23±5°C, 45±20%RH.
- (6) When turning the power on, input each signal after the positive/negative voltage becomes stable.

#### 11.5 SAFETY

- (1) It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- (2) If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

JHB12232D VER: 3.01 - 20 - Issue date: 2013/08/01

# 12. REVISION HISTORY

| Version | Revise record                                     | Date     |
|---------|---|----------|
| 1.0     | Original version                                  | 07-09-15 |
| 2.0     | Change LCM configuration with PCB                 | 09-04-15 |
| 3.0     | Change the ic from SED1520 to SBN1661G-M18        | 09-12-03 |
| 3.01    | Perfect the VER3.0 spec, Commany internal modify. | 13-08-01 |
|         |   |          |
|         |   |          |
|         |   |          |
|         |   |          |
|         |   |          |
|         |   |          |
|         |   |          |
|         |   |          |
|         |   |          |
|         |   |          |
|         |   |          |

JHB12232D VER: 3.01 - 21 - Issue date: 2013/08/01



# SAMPLE APPROVED REPORT

(样品确认单)

| SAMPLE MODEL NO. (样品型号)                 | JHB12232D             |  |  |  |
|---|-----------------------|--|--|--|
| SAMPLE SERIES NUMBER NO. (样品序号)         |                       |  |  |  |
| SAMPLE QUANTITY (样品数量)                  |                       |  |  |  |
| COLOR/TYPE (底色/类型)                      | STN(Y-G)/POSITIVE     |  |  |  |
| VIEWING DIRECTION (视角)                  | 6:00                  |  |  |  |
| DRIVING METHOD (驱动参数)                   | 1/32Duty,1/6Bias      |  |  |  |
| ILOGIC VOLTAGE (IC 工作电压)                | 5.0V                  |  |  |  |
| LCD VOP (LCD 驱动电压)                      | 4.5V                  |  |  |  |
| OPERATING TEMP. (操作温度)                  | -20 ~ 70℃             |  |  |  |
| STORAGE TEMP. (储存温度) ℃                  | -30 ~ 80℃             |  |  |  |
| POLARIZER MODE (偏光片类型)                  | TRANSFLECTIVE         |  |  |  |
| CONTROLLER/DRIVER IC(控制/驱动 IC)          | SBN1661G-M18 or EQUIV |  |  |  |
| BACKLIGHT COLOR/TYPE (背光源类型/颜色)         | LED/Y-G               |  |  |  |
| DRAWING REV/NO./QUANTITY (图纸版本/数量)      |                       |  |  |  |
| SPECIFICATION (规格书 份数)                  |                       |  |  |  |
| REMARKS:                                |                       |  |  |  |
| (备注)                                    |                       |  |  |  |
| WRIT BY: DATE: APROV BY:                | DATE:                 |  |  |  |
| CUSTOMER'S APPROVAL (客户确认):             |                       |  |  |  |
| 1) FUNCTION (功能): □ OK □ N.G.           |                       |  |  |  |
| 2) DRIVER CONDITION (驱动条件): □ OK □ N.G. |                       |  |  |  |
| 3) DISPLAY MODE (显示模式): □ OK □ N.G.     |                       |  |  |  |
| 4) VIEWING ANGLE (视角): □ OK             | $\square$ N.G.        |  |  |  |
| 5) BACKLIGHT (背光源): □ OK                | □ N.G.                |  |  |  |
| 6) DISPLAYING PATTERN (显示效果): □ (       | OK □ N.G.             |  |  |  |
| CUSTOMER'S CONCLUSIONS (客户意见):          |                       |  |  |  |
|   |                       |  |  |  |
|   |                       |  |  |  |
|   |                       |  |  |  |
| CUSTOMER'S SIGNATURE(客户签名): DATE (日期):  |                       |  |  |  |
|   |                       |  |  |  |